

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Geeng-Chuan Chern

Application No. 09/916,618

Filed: July 26, 2001

For: SEMICONDUCTOR MEMORY ARRAY
OF FLOATING GATE MEMORY CELLS
WITH VERTICAL CONTROL GATE
SIDEWALLS AND INSULATION
SPACERS
(as amended)

Group Art Unit: 2814

Examiner: Howard Weiss

**LETTER TO OFFICIAL
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Box, Assistant Commissioner for Patents, Washington, DC 20231, on September 25, 2002.

GRAY CARY WARE & FREIDENRICH Date: 09/25/02

By: Kathleen LaBrie
Kathleen LaBrie

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

With the permission of the Examiner, please approve the proposed drawing changes identified below and indicated in red on the attached copies of Figs. 1A, 1B and 1C:

Please add --(Prior Art)-- to the legends of Figures 1A-1C.

Respectfully submitted,

GRAY CARY WARE & FREIDENRICH

Dated: Sept 25, 2002

By: Alan A. Limbach

Alan A. Limbach
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Attorneys for Applicant(s)



Amend
12/1/02

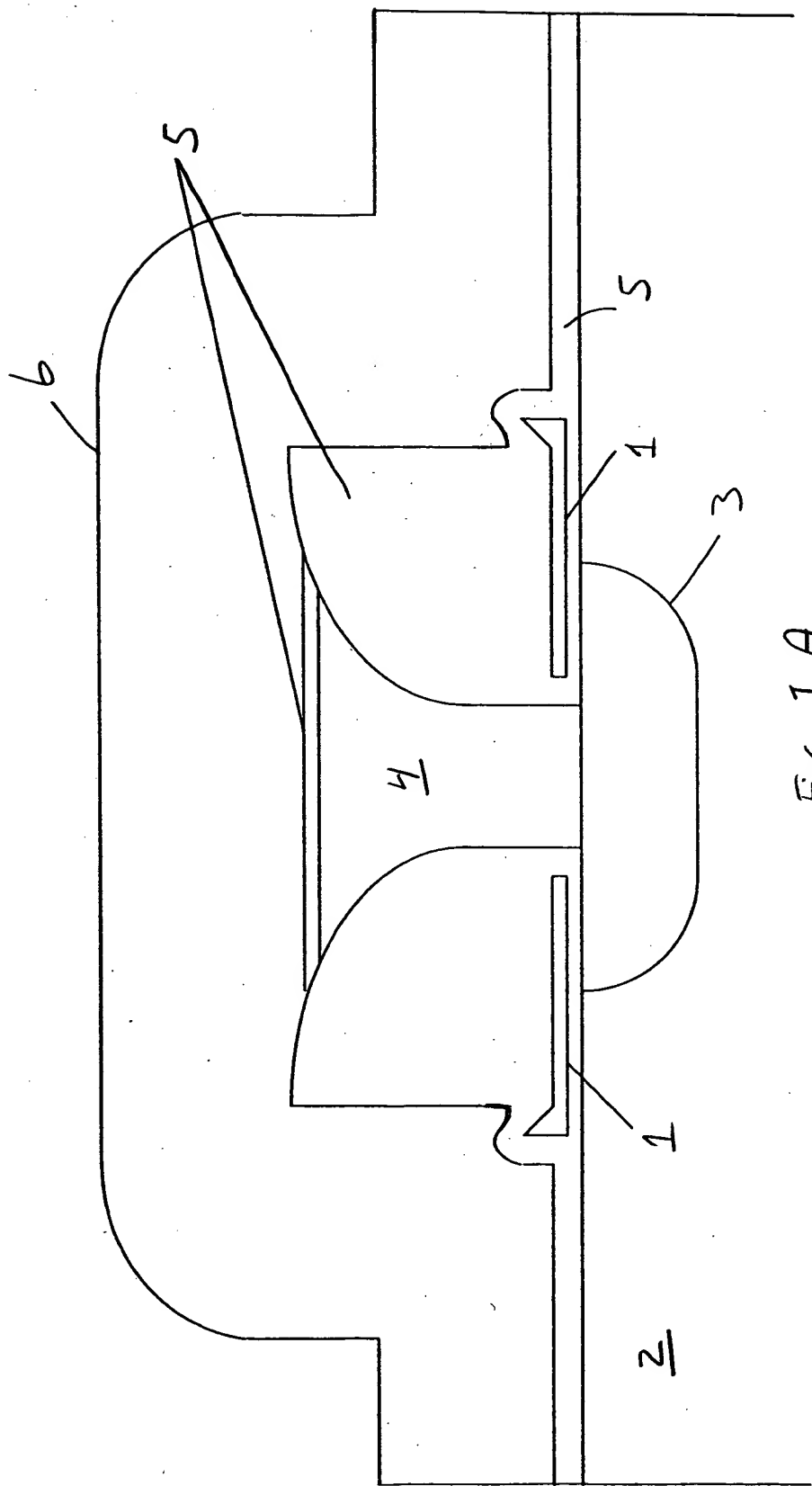


Fig 1A
(Prior Art)



Approved
12/5/02

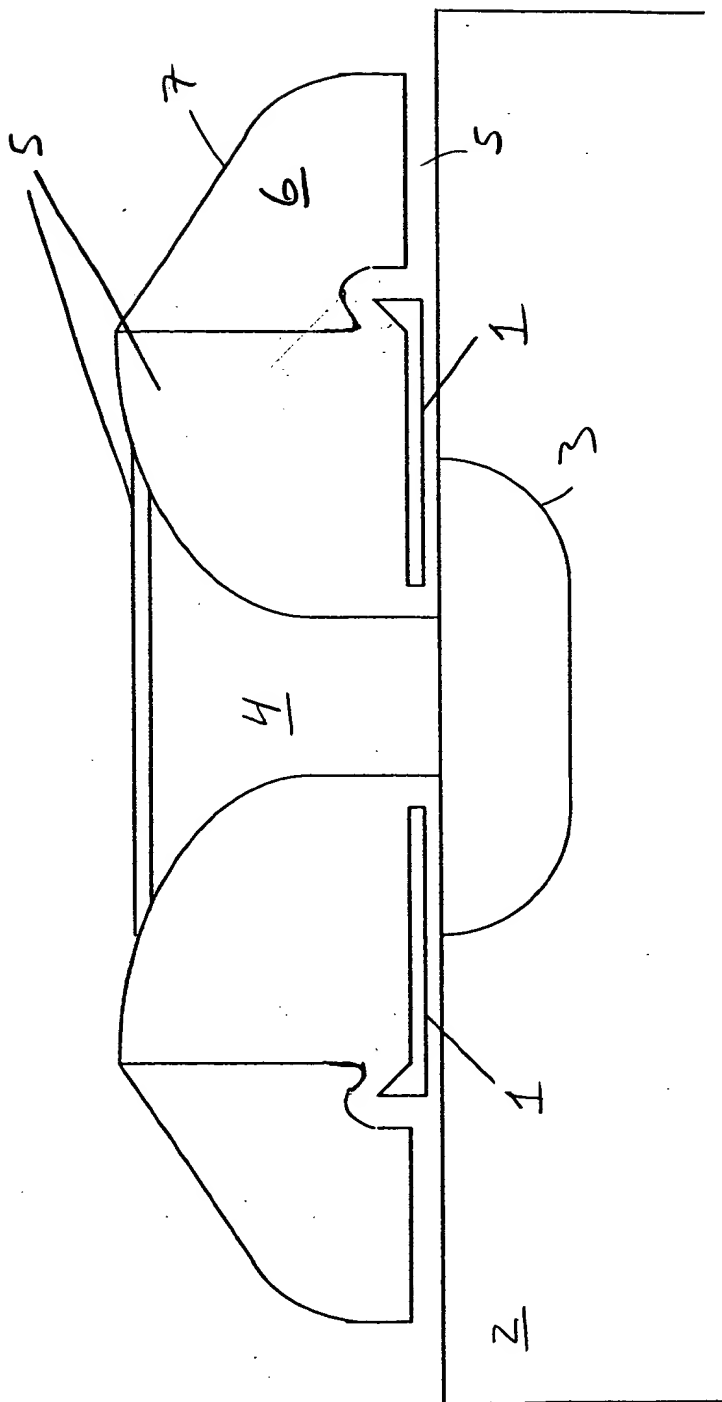


Fig 1B
(Prior Art)



Amal
12/5/02

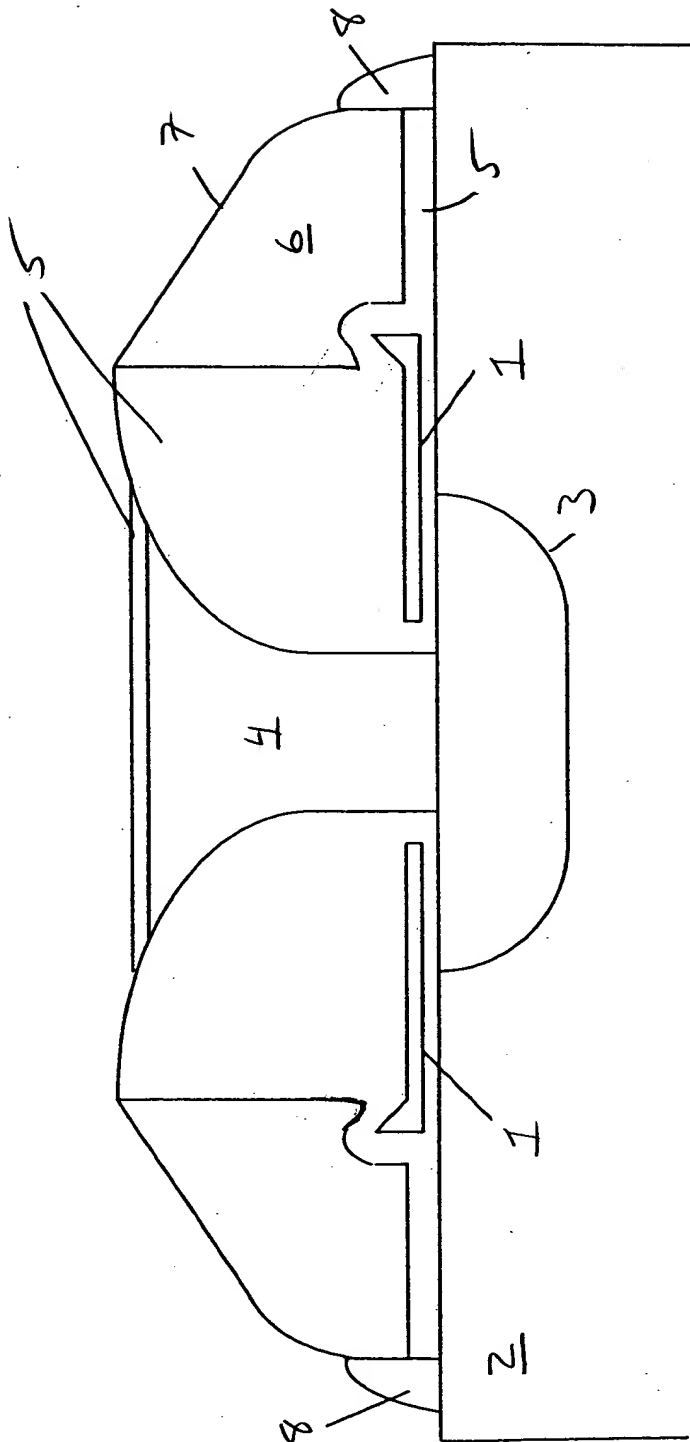


Fig 1C
(Prior Art)